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APPLICATION NO. 09/702320	CONT/PRIOR	CLASS 712	SUBCLASS 265	2 1 8 3	Chartolie
APPLICANTS Richard :	Simar, J Brown	r .			

TITLE

Microprocessor with execution packet spanning two or more fetch packets $\dot{\ }$

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ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
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TERMINAL DISCLAIMER		DRAWINGS		CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent				NOTICE OF ALLOWANCE MAILED		
ubsequent to (date) as been disclaimed.	(Assistant	Examiner)	(Date)			
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ARNING: ne information disclosed herein may be re- ossession outside the U.S. Patent & Trader	stricted. Unauthorized	disclosure may be	prohibited by the loyees and contract	United States Code Title	35, Sections 122, 181 and 36	

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